PATENT NUMBER and ISSUE DATE

U.S. UTILITY Patent Application

	S: Syring	361	695	2835	FOLIN	
	DATA VERIFIED	Harold; Ka	arl Rex;			
	ICATIONS VERIF	IED:	-			
S-PUB DO NOT F eign priority claimed USC 119 conditions m fied and Acknowledge	et Examiners's intigo	⊒ yes □ no ⊒ yes □ no	RESCIND C	ATTO	RNEY DOCKET NO	
E: Device for co	oling CPU chip				1 619503-1 T. OF COMM /PAT. 8 TM-PTO-436L(Rev. 12	-94)

NOTICE OF ALLOWANCE MAILED			CLAIMS ALLOWED		
199	UE FEE	Assistant Examiner	Total Claims	Print Claim for O.G	
Amount Due Date Paid			DRAWING		
			Sheets Drwg. Figs.	Drwg. Print Fig	
TERMINAL DISCLAMER		Primary Examiner		13/1	
		PREPARED FOR ISSUE	Application Examiner		
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